MDL8, MDL24W(A) Series



High Efficiency Step Down LED Driver

Features

- RoHS-compliant 16 Pin DIL Package
- Constant Current Output (±8% Output Current Accuracy)
- LED Driver Current 150/250/300/350/500/600/700/1000 mA
- Power LED Driver
- Wide Input Voltage Range: 7V to 30V (40V for 0.5sec)
- Output Power 4.2/7/8/14/17/20/24 W
- Driver LED Strings of up to 28V (2V to 28V)
- High Efficiency (up to 95%)
- PWM/Digital Dimming and Analog Voltage Dimming
- Open and Short LED Protection
- -40°C ~ 85°C Operation Temperature Range
- With MLCC Capacitors only
- IP67 rated



Application

- 12V and 24V Lighting Systems
- Household/Commercial lighting
- Suitable for high illumination LED
- Power limited (battery) lighting system

MDL8,MDL24W(A) Series is a high efficiency step-down converter optimized to drive high current LEDs. The control algorithm allows highly efficient and accurate LED current regulation. The device operates from an input 7Vdc to 30Vdc and provides an externally adjustable output current up to 1000mA and output power up to 24 watts. Compact size of DIL16 allows designer to integrate this driver together with LED module. UL94V-0 grade molded case with high grade filling material provide excellent fire proof characteristics.

Typical at Ta = +25°C, nominal input voltage, rated output current unless otherwise specified.

Electrical Specifications:	
Input Voltage (Vdc)	7V ~ 30V, 24Vdc Nominal
Input Filter	Capacitor
Input Current (No-Load)	1.5mA, max.
Output Voltage Range (Vin = 30V)	2V to 28V
Output Current Range (Vin - Vout > 3V)	See table
Output Current Accuracy	See table, max.
Output Power	See table, max.
Ripple and Noise (20 MHz bandwidth)	See table, max.
Efficiency	95%, max.
Capacitive Load	47μF, max.
Operating Frequency	40 kHz ~ 370 kHz
Short Circuit Protection Re	gulated at Rated Output Current
Temperature Coefficient	±0.08%/°C, max.
Thermal Impedance (Nature Convection)	+50°C/W
Safety Standard (designed to meet)	IEC / EN 60950-1

Dimming Control and ON/OFF Control (Leave Open if Not Used):				
V _{ADJ} Pin Input Voltage Range	0V to 1.25V			
Vady Pin Drive Current (Vady = 1.25V)	<1mA			
Analog Dimming				
Adjust Output Current (Vin - Vout < 20V)	25% to 100%			
Control Voltage Range Limits				
On	0.3V < VADJ < 1.25V			
Off	VADJ < 0.15V			
PWM Dimming				
Recommended Maximum Operation Frequence	v 1KHz			
Adjust Output Current	0% to 100%			
Remote ON/OFF				
DC/DC ON 0.3V < VADJ <	1.25V or open circuit			
DC/DC OFF (Shutdown) VADJ < 0.15V or Shor	t circuit wire 1 and wire 2			
Quiescent Input Current in Shutdown Mode (Vin = 30)	v) 25uA. max.			

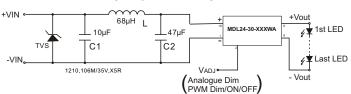
Environmental Specifications	
Operating Temperature Range	-40°C to +85°C(See Derating Curve)
Storage Temperature Range	-40°C to +125°C
Water Resistance	IP67
Maximum Case Tempeature	+100°C
Cooling	Nature Convection
Reliability Calculated MTBF(MIL-HDBK	(-217 F) >1.6 Mhrs
Soldering Temperature (1.5mm from case	+260°C, max.

Physical Specifications					
Case Material	Non-Conductive Black Plastic(UL94V-0 rated)				
Potting Material	Epoxy (UL94V-0 rated)				
Lead Wires	UL1015/CSATEM listed/22AWG/600V/105°C Rated				
Weight					
MDL8W(A)	9.7g/10.7g				
MDL24W(A)	10.7g/11.7g				
Dimensions	0.92"x0.55"x0.40"				

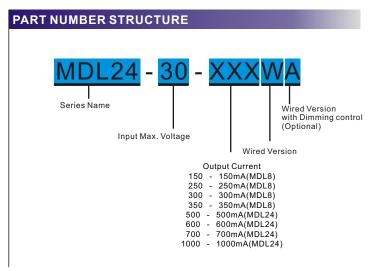
EMC SPECIFICATIONS	
EMI Radiated & Conducted Emissions	EN 55015 (CISPR22)
EMS Immunity EN 61547	
IEC 61000-4-2	Perf. Criteria A
IEC 61000-4-3	Perf. Criteria A
IEC 61000-4-4	Perf. Criteria A
IEC 61000-4-6	Perf. Criteria A
IEC 61000-4-8	Perf. Criteria A

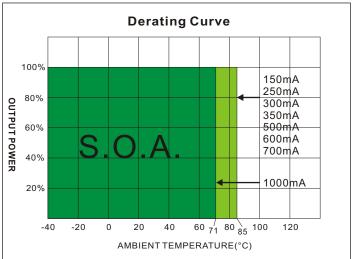
NOTE

- 1.Reversed power source damages the circuit, No connection is allowed between input ground and output.
- 2.DO NOT operate the driver over output power.
- 3. Leave pin VADJ open if not in use, ground pin to shut down the converter. Connecting Vadj to Vin damages the circuit.
- 4.Maximum output open voltage is equal to input voltage.
- 5. Input filter components (C1, L, C2) are used to help meet conducted emissions requirement for the module.
- 6.For the compliance with IEC61000-4-5, a TVS is thus recommended to be installed in from of the input filter, the reference model: 3.0SMCJ24A or SMCJ24A (TVS Max Clamping Voltage @ Max Peak Pulse





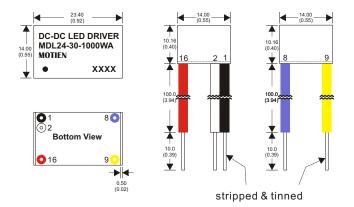




MODEL SELECTION GUIDE

	INPUT	OUTPUT		OUTPUT Current	OUTPUT	EFFICIENCY		Capacitor
MODEL NUMBER	Voltage Range	Voltage Range	Current	Accuracy	Power	@FL	Ripple and Noise	Load @FL
	(Vdc)	(Vdc)	(mA)	(%, max.)	(W, max.)	(%, max.)	(mVp-p, max.)	(μF, max.)
MDL8-30-150W(A)	7 - 30	2 - 28	150	±10	4.2	67 - 95	200	47
MDL8-30-250W(A)	7 - 30	2 - 28	250	±8	7	67 - 95	200	47
MDL8-30-300W(A)	7 - 30	2 - 28	300	±7	8	67 - 95	200	47
MDL8-30-350W(A)	7 - 30	2 - 28	350	±6	8	67 - 95	200	47
MDL24-30-500W(A)	7 - 30	2 - 28	500	±8	14	75 - 95	250	47
MDL24-30-600W(A)	7 - 30	2 - 28	600	±8	17	75 - 95	250	47
MDL24-30-700W(A)	7 - 30	2 - 28	700	±8	20	75 - 95	250	47
MDL24-30-1000W(A)	7 - 30	2 - 28	1000	±8	24	75 - 95	300	47

MECHANICAL DIMENSION



Lead wires are under the specification of general lamps: Wire is UL1015/CSA TEM listed #22AWG / 600V / 105°C Rated

16 Pin DIL Package wired version

Notes: All dimensions are typical in millimeters (inches).

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 1. Wire core diameter: 0.80±0.1 (0.31±0.004)

 2. Wire outside diameter: 2.4±0.1 (0.094±0.004)

 3. Wire length = 100 + 10 stripped & tinned = 110±5 total

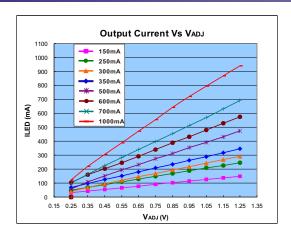
 4. Case Tolerance: ±0.5 (±0.02)

\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\		CONNECTIONS			
Wire #		MDL24-30-XXXWA	MDL24-30-XXXW		
1 (Black)	- V Input	- DC Supply	- DC Supply		
2 (White)	VADJ	PWM/ON/OFF or not used	No wires		
8 (Blue)	- V Output	LED Cathode Connection	LED Cathode Connection		
9 (Yellow)	+V Output	LED Anode Connection	LED Anode Connection		
16 (Red)	+V Input	+DC Supply	+DC Supply		

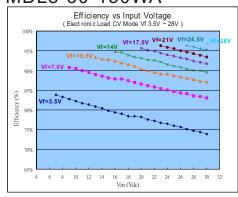
No connection is allowed between input and output



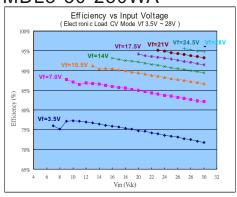
Typical electrical characteristic curves



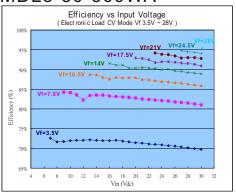
MDL8-30-150WA



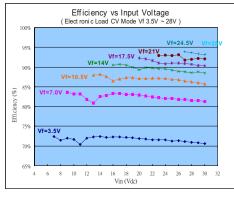
MDL8-30-250WA



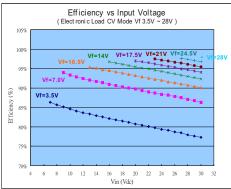
MDL8-30-300WA



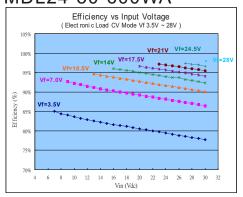
MDL8-30-350WA



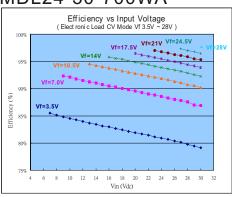
MDL24-30-500WA



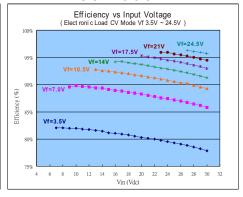
MDL24-30-600WA



MDL24-30-700WA



MDL24-30-1000WA



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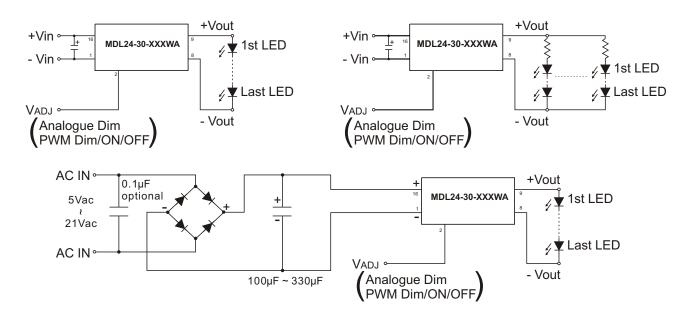
DRAWING:

APPROVED:

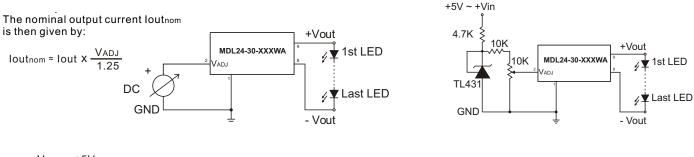
Last Update: Nov.19.2015

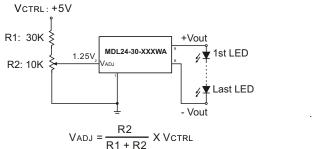


Typical Application



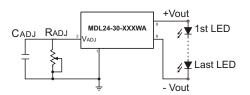
Output Current Adjustment By External DC Control Voltage





Resistor dimming

By connecting a variable resistor between ADJ and GND, simple dimming can be achieved. Capacitor CADJ is optional for better AC mains interference and HF noise rejection. Recommend value of CADJ is $0.22\mu F$.



The current output loutnom can be determined using the equation:

$$Ioutnom = \frac{Iout X RADJ}{(RADJ + 200K)}$$

If the value of RadJ is 0 to 2M ohm, the maximum adjust range of output current is 25% to 90%. (For Vin-Vout<20Vdc)



Typical Application

GND

Output Current Adjustment By PWM Control

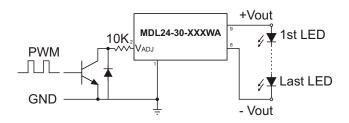
Directly driving ADJ input

A Pulse Width Modulated (PWM) signal with duty cycle DPWM can be applied to the ADJ pin, as shown below

Iout_{nom} ≈ Iout x DPWM [If PWM frequency <200Hz, for 0.1<DPWM<1]

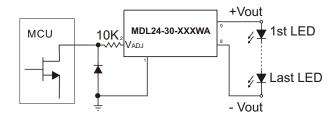
1.25V PWM MDL24-30-XXXWA +Vout 1st LED

Driving the ADJ input via open collector transistor The diode and resistor suppress possible high amplitude negative spikes on the ADJ input resulting from the drain-s ource capacitance of the transistor. Negative spikes at the input to the device should be avoided as they may cause errors in output current, or erratic device operation.



Driving the ADJ input from a microcontroller

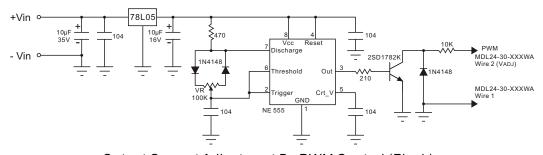
Another possibility is to drive the device from the open drain output of a microcontroller. The diagram below shows one method of doing this:



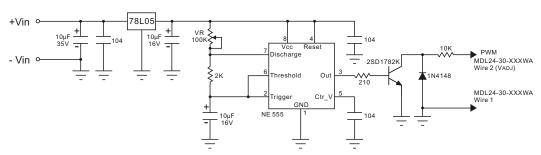
The diode and resistor suppress possible high amplitude negative spikes on the ADJ input resulting from the drain-s ource capacitance of the FET. Negative spikes at the input to the device should be avoided as they may cause errors in output current, or erratic device operation.

Output Current Adjustment By PWM Control (Dimming)

To avoid visible flicker the PWM signal must be greater than 100Hz.



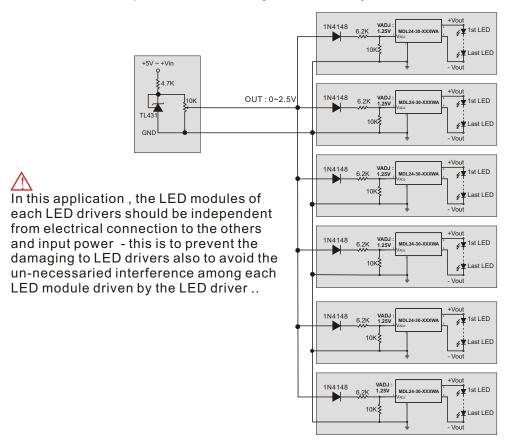
Output Current Adjustment By PWM Control (Flash)



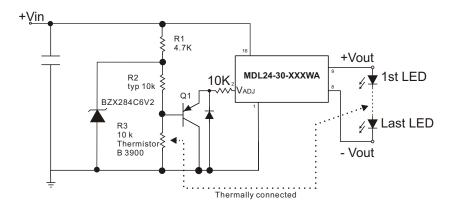


Typical Application

Output Current Adjustment By External DC Control Voltage



Thermal feedback circuit



The selection of components for the thermal feedback circuit is not only dependent on the choice of R2 and R3, but also on the amount of heat sink area required to extract heat from the LEDs. To maximize the light output at high ambient or operating temperature conditions, the LEDs must have a sufficient thermal extraction path, otherwise the thermal control circuit will effect current drive reduction in non-optimal conditions. The thermal control threshold point is set by adjusting R2. For this design, three values (33k, 22k and 10k) were evaluated. These values were chosen to give break points at approximately 25°C, 40°C and 60°C.

Note that the light output will not continually dim to zero - the thermal control is applying DC control to the ADJ pin and therefore has a dimming ratio from maximum Current of approximately 5:1. Once the reduced DC level goes below the shutdown threshold of around 200mV, the LED drive current will fall to zero and the LEDs will be extinguished. The slope of the current reduction is determined by the beta value of the thermistor. The larger the beta value, the sharper will be the resultant current control response. The slope of the current reduction is also affected by Q1's base emitter voltage (VBE) variation with temperature.