T1 Series



1W High temperature Single & Dual output

Features

- SIP7 Package
- 1500 VDC Isolation
- Up to 3000 VDC Isolation
- Continuous Short Circuit Protection
- Efficiency up to 82%
- Operation Temperature Range -40 ~ 105°C max.
- Non-Conductive Black Plastic Case



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PART NUMBER STRUCTURE $\frac{T1}{(1)}$ - $\frac{12}{(2)}$ $\frac{05}{(3)}$ $\frac{SS}{(4)}$ $\frac{H}{(5)}$ (1) Series (2) Input Voltage Range (4) Case & Output Type 05 - 4.5-5.5 V S - SIP Case, Dual Output **12 -** 10.8-13.2 V SS - SIP Case, Single Output **24 -** 21.6-26.4 V (5) Isolation Voltage (Optional) Blank - 1.5 KVDC (3) Output Voltage 05 - 5.0 V H- 3 KVDC 12 - 12 V 15 - 15 V

ALL SPECIFICATIO	ONS ARE TYPICA	L AT 25°C,	NOMINAL IN	IPUT AND FU	LL LOAD UNLE	SS OTHERN	VISE NOTED
Model Number	Input Voltage Range (VDC)	Input C No-Load (mA, max.)	urrent Full Load (mA, typ.)	Output Voltage (VDC)	Output Current Full Load (mA)	Efficiency @FL (%, typ.)	Capacitive Load @ FL (µF, max.)
T1-0505SS	4.5-5.5	40	247	5	200	81	220
T1-0512SS	4.5-5.5	40	247	12	83.3	81	100
T1-0515SS	4.5-5.5	40	247	15	66.7	81	100
T1-1205SS	10.8-13.2	18	104	5	200	80	220
T1-1212SS	10.8-13.2	18	102	12	83.3	82	100
T1-1215SS	10.8-13.2	18	103	15	66.7	81	100
T1-2405SS	21.6-26.4	9	51	5	200	81	220
T1-2412SS	21.6-26.4	9	52	12	83.3	80	100
T1-2415SS	21.6-26.4	9	52	15	66.7	80	100
T1-0505S	4.5-5.5	40	247	±5	±100	81	±100
T1-0512S	4.5-5.5	40	247	±12	±41.67	81	±47
T1-0515S	4.5-5.5	40	244	±15	±33.33	82	<u>+</u> 47
T1-1205S	10.8-13.2	18	104	±5	±100	80	±100
T1-1212S	10.8-13.2	18	102	±12	±41.67	82	<u>+</u> 47
T1-1215S	10.8-13.2	18	102	±15	±33.33	82	±47
T1-2405S	21.6-26.4	9	53	±5	±100	79	±100
T1-2412S	21.6-26.4	9	51	<u>+</u> 12	±41.67	81	±47
T1-2415S	21.6-26.4	9	51	±15	±33.33	81	<u>+</u> 47

T1-1W 1W High Temperature Single & Dual output



Parameter	Conditions	Min.	Тур.	Max.	Unit	
	5 V Input	4.5	5	5.5		
Input Voltage Range	12 V Input	10.8	12	13.2	VDC	
	24 V Input 21.6 24		24	26.4	1	
Input Filter			Capacitor			
Input Reflected Ripple Current (1)			15		mApk-pk	
Start up Time	Nominal Vin and constant resistive load		20		ms	
	5 V Input	0.75				
Recommended input fuse (slow blow)	12 V Input		0.3			
	24 V Input		0.15		1	

Note :

1. Measured with a simulated source inductance of 12μ H and a source capacitor Cin (47μ F, ESR< 1.0Ω at 100kHz).

OUTPUT SPECIFICATIONS

Parameter	Conditions		Min.	Тур.	Max.	Unit	
Output Voltage Accuracy			See tolerance envelope curve				
Line Regulation	For 1% Vin Change		-1.2		+1.2	%	
Lood Deculation	From 10% to 100% Load	5 V Input			10	- %	
Load Regulation		Other Input			7.5		
Cross Regulation	Asymmetrical Load 25% / 100% for Dual Output		-4		+4		
Ripple & Noise (1)	20MHz bandwidth				75	mVpk-pk	
Short Circuit Protection				Continuous	auto recovery,	,	
Temperature Coefficient			-0.02		+0.02	%/°C	
Maximum Capacitive Load Minimum Vin and constant resistive load			See	e Table			
Note :			•				
1 Measured with a 0 1µE MI CC and 1	10uE Electrolytic canacitor						

1. Measured with a 0.1µF MLCC and 10µF Electrolytic capacitor.

ABSOLUTE MAXIMUM RATINGS					
Parameter	Conditions	Min.	Тур.	Max.	Unit
Input Surge Voltage (1 sec)	5 V Input			9	
	12 V Input			18	VDC
	24 V Input			30	
Soldering Temperature	1.5mm from case 10sec max.			260	°C
Note : These are stress ratings. Exposure of devices to any of these conditions may adversely affect long-term reliability.					

Note : These are stress ratings. Exposure of devices to any of these conditions may adversely affect long-term reliability.

GENERAL SPECIFICATIONS						
Parameter	Conditions		Min.	Тур.	Max.	Unit
Isolation Voltage	Input-output, and rated for 60sec	Standard Type	1500			VDC
		Suffix "H"	3000			
Isolation Resistance	Input-output		1000			MΩ
Isolation Capacitance	Input-output			50		pF
Switching Frequency				50		kHz
MTBF	MIL-HDBK-217 F @ 25°C		3.6			M hours
Safety Standard IEC / EN / UL 62368-1			Designed to meet			
Environmental compliance				R	oHS	

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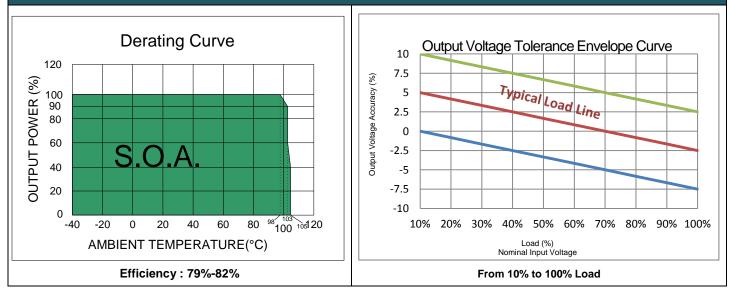


ENVIRONMENT SPECIFICATIONS						
Parameter	Conditions	Min.	Тур.	Max.	Unit	
Operating Ambient Temperature	See the Derating Curve	-40		105	°C	
Maximum Case Temperature				115	°C	
Thermal Impedance		46.22			°C/W	
Storage Humidity				95	% rel. H	
Storage Temperature		-55		125	°C	
Cooling	Natural Convection		30-6	5 LFM		

EMC SPECIFICATIONS						
Parameter	Standard	Condition	Criterion			
Conducted Emissions	EN55032	with external components	В			
Radiated Emissions	EN55032		В			
ESD	IEC 61000-4-2	Air ±15kV / Indirect: ±8kV	А			
RS	IEC 61000-4-3	10V/m	А			
EFT	IEC 61000-4-4	±2.0kV	А			
Surge	IEC 61000-4-5	±1.0kV with external components	А			
CS	IEC 61000-4-6	10Vrms	А			
PFMF	IEC 61000-4-8	30A/m	А			

PHYSICAL SPECIFICATIONS				
Parameter	Value			
Case Material	Nonconductive Black Plastic (UL94V-0 rated)			
Pin Material	Tinned copper			
Potting Material	Epoxy (UL94V-0 rated)			
Weight	2.4 g, typ.			
Dimensions	0.76" x 0.24" x 0.39"			

ELECTRICAL CHARACTERISTIC CURVES

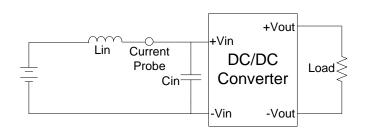




TEST CONFIGURATIONS

Input Reflected Ripple Current Test Step

Input reflected ripple current is measured with a source inductor Lin (12μ H) and a source capacitor Cin (47μ F, ESR<1.0 Ω at 100kHz) at nominal input and full load.



DESIGN & FEATURE CONFIGURATIONS

Isolation Voltage

This series is designed to meet the functional insulation of UL, both input and output should be maintained within SELV limits (less than 42.4V peak, or 60VDC).

The isolation test voltage represents a measure of immunity to transient voltages and the part should never be used as an element of a safety isolation system. The part could be expected to function correctly with hundreds of volts offset applied continuously across the isolation barrier; but then the circuitry on both sides of the barrier must be regarded as operating at an unsafe voltage and further isolation/insulation systems must form a barrier between these circuits and any user-accessible circuitry according to safety standard requirements.

Repeated High-Voltage Isolation Testing

Repeated high-voltage isolation testing of a barrier component can actually degrade isolation capability, to a lesser or greater degree depending on materials, construction and environment.

This series has isolation transformers without additional insulation between primary and secondary windings of enameled wire.

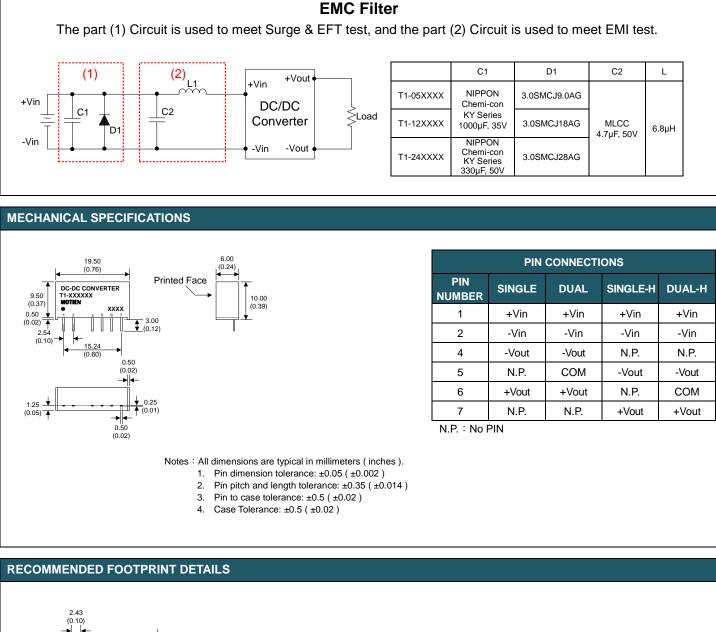
While parts can be expected to withstand several times the stated test voltage, the isolation capability does depend on the wire insulation.

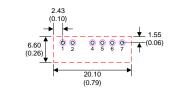
Any material including the enamel (typically polyurethane) is susceptible to eventual chemical degradation when subject to very high applied voltage, thus implying that the number of tests should be strictly limited.

We strongly advise against repeated high voltage isolation testing, but if it is absolutely required, the isolation test voltage should be reduced by 20% from specified test voltage.



DESIGN & FEATURE CONFIGURATIONS





Notes : 1. All dimensions are typical in millimeters (inches). Through hole (black) 1~7:Ø0.80 (0.031) Top view pad (green) 1~7:Ø1.00 (0.039) Bottom view pad (pink) 1~7:Ø1.60 (0.063)



ISO 9001 . ISO 14001 . IECQ QC080000

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